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LSI Logic Corporation
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1621 Barber Lane, MS D-106
Milpitas, CA 95035Date December 2, 2004Number of pages including cover sheet 2

To: US PTO

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TC 2819

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From: Connie del Castillo

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REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please commentApplication Number: 10/724,949
Filing date: December 1, 2003
First named inventor: Andreas Hils et al.
Attorney docket number: 03-0351 / 1496.00326

Transmitted herewith for filing via facsimile:

- Status Request Letter

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Andreas Hils et. al.

Serial No. : 10/724,949

Filed : December 01, 2003

For : Integrated Circuits And Design And
Manufacture Thereof

Group Art Unit: 2819

Examiner :

Atty Docket : 1496.00326 / 03-0351

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STATUS REQUEST

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

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Date: 12/02/04

Respectfully submitted,

Peter Scott, Ph: [+1] 408-433-7191

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